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Electronic Version

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Title of Invention

[METHOD OF FABRICATING THIN FILM TRANSISTOR ARRAY
SUBSTRATE AND STACKED THIN FILM STRUCTURE]

Submission Type : Utility Patent Filing

Application Number:

10/709089



EFS ID:

58878

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ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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First Named Applicant: Han-Chung Lai

Attorney Docket Number: 12406-US-PA

Timestamp: 2004-04-13 05:57:31 EDT

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-assignment	12406-usassn.xml	1545	2004-04-13
us-assignment	us-assignment.xsl	8983	2004-04-13
us-assignment	us-assignment.dtd	10898	2004-04-13
us-assignment	12406assignment01.tif	54170	2004-04-13
us-assignment	12406assignment02.tif	7780	2004-04-13
us-request	12406-usrequ.xml	1745	2004-04-13
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us-request	us-request.xsl	33300	2004-04-13
us-fee-sheet	12406-usfees.xml	1758	2004-04-13
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us-fee-sheet	us-fee-sheet.dtd	11069	2004-04-13
us-declaration	12406declaration01.tif	43052	2004-04-13
us-declaration	12406declaration02.tif	20902	2004-04-13
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application-body	isoamsc.ent	1460	2004-04-13
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package-data	12406-pkda.xml	10103	2004-04-13
package-data	package-data.dtd	27025	2004-04-13
package-data	us-package-data.xsl	19263	2004-04-13
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